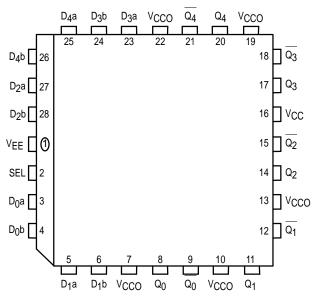
5-Bit 2:1 Multiplexer

The MC10E/100E158 contains five 2:1 multiplexers with differential outputs. The output data are controlled by the Select input (SEL).

- 600ps Max. D to Output
- 800ps Max. SEL to Output
- Differential Outputs
- One VCCO Pin Per Output Pair
- Extended 100E V_{EE} Range of 4.2V to 5.46V
- 75kΩ Input Pulldown Resistors

Pinout: 28-Lead PLCC (Top View)



* All V_{CC} and V_{CCO} pins are tied together on the die.

PIN NAMES

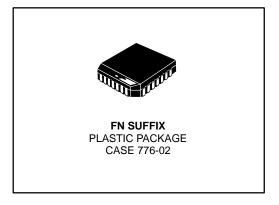
| Pin | Function | | | | | | |
|-------------------------------------|------------------|--|--|--|--|--|--|
| D ₀ a – D ₄ a | Input Data a | | | | | | |
| D ₀ b – D ₄ b | Input Data b | | | | | | |
| SEL | Select Input | | | | | | |
| $Q_0 - Q_4$ | True Outputs | | | | | | |
| $Q_0 - Q_4$ | Inverted Outputs | | | | | | |

FUNCTION TABLE

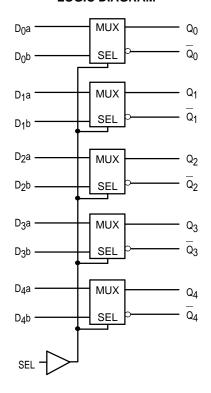
| SEL | Data |
|-----|------|
| Н | а |
| L | b |

MC10E158 MC100E158

5-BIT 2:1 MULTIPLEXER



LOGIC DIAGRAM





REV 2

12/93

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DC CHARACTERISTICS ($V_{EE} = V_{EE}(min)$ to $V_{EE}(max)$; $V_{CC} = V_{CCO} = GND$)

| | | 0°C | | 25°C | | | 85°C | | | | | |
|--------|----------------------|-----|-----|------------|-----|-----|------------|-----|-----|------------|------|-----------|
| Symbol | Characteristic | min | typ | max | min | typ | max | min | typ | max | Unit | Condition |
| lН | Input HIGH Current | | | | | | | | | | μΑ | |
| | D SEL | | | 200 150 | | | 200 150 | | | 200 150 | | |
| IEE | Power Supply Current | | | | | | | | | | mA | |
| | 10E | | 33 | 40 | | 33 | 40 | | 33 | 40 | | |
| | 100E | | 33 | 40 | | 33 | 40 | | 38 | 46 | | |

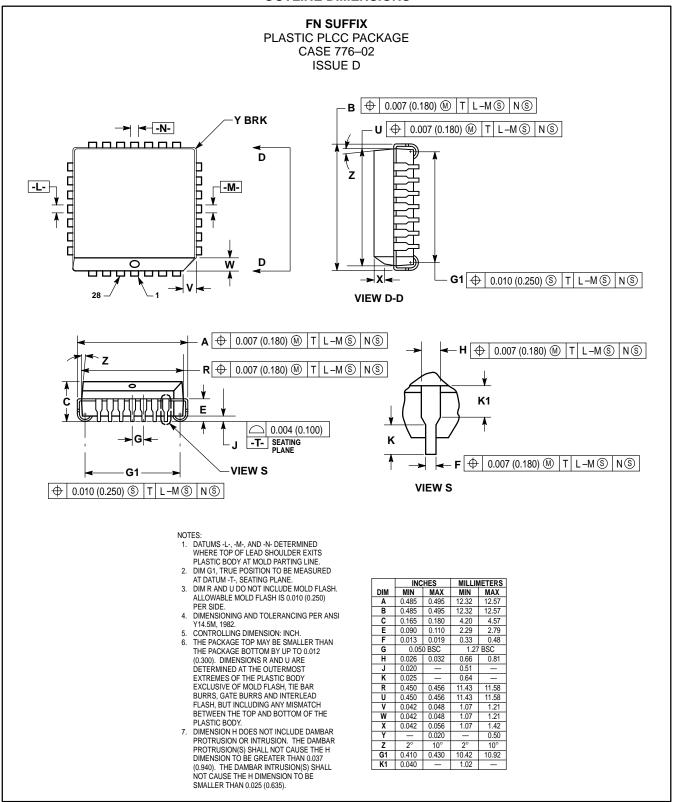
AC CHARACTERISTICS ($V_{EE} = V_{EE}(min)$ to $V_{EE}(max)$; $V_{CC} = V_{CCO} = GND$)

| | | 0°C | | | 25°C | | | 85°C | | | | |
|--------------------------------------|---|------------|------------|------------|------------|------------|------------|------------|------------|------------|------|-----------|
| Symbol | Characteristic | min | typ | max | min | typ | max | min | typ | max | Unit | Condition |
| ^t PLH ^t PHL | Propagation Delay to Output D SEL | 225 400 | 385 600 | 550 775 | 225 400 | 385 600 | 550 775 | 225 400 | 385 600 | 550 775 | ps | |
| tSKEW | Within-Device Skew | | 60 | | | 60 | | | 60 | | ps | 1 |
| t _r | Rise/Fall Time 20 - 80% | 275 | 425 | 650 | 275 | 425 | 650 | 275 | 425 | 650 | ps | |

^{1.} Within-device skew is defined as identical transitions on similar paths through a device.

MOTOROLA 2–2

OUTLINE DIMENSIONS



MC10E158 MC100E158

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